# ARMAN SOLTANI

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www.armansoltani.ir ⋄ Linkedin

## EDUCATION

**Shiraz University, Shiraz, Iran** *September 2014 - January 2019*
Bachelor of Engineering, Electronics                                    GPA: 14.51/20
Department of Electrical and Computer Engineering
**Shahid Dastgheib 2 High School, Shiraz, Iran** *September 2010 - June 2014*
Mathematics and Physics                                    GPA: 18.95/20
National Organization for Development of Exceptional Talents

## HONORS

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|---|---:|
| Member of IEEE Shiraz Student Branch | 2016 - 2019 |
| Member of the Scientific Association of Computer and Electrical science (SAEE) | 2015 - 2019 |
| Member of first top first 1/5 of students in university | 2019 |
| Received full scholarship from "The Shiraz University" (tuition-free) | 2014 - 2019 |
| Member of the first 1% group of bachelor degree entrance exam participants | 2014 |
| Accepted in first stage of Mathematics Olympiad for students in high school | 2011 , 2012 |
| Participation in Robotic Cup | Iran Open 2010 |

## RESEARCH INTERESTS

- Artificial Intelligence
- Machine Learning
- Natural language processing
- Deep Learning
- Image Processing

## SKILLS

| | | |
|---|---|---|
| **Python** | **C++** | **JAVA** |
| **Matlab** | **Verilog/VHDL** | **JS** |
| **C#** | **Assembly** | **Microsoft Office** |
| **C** | **HTML/CSS** | **MySQL** |

## PROJECT

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|---|---|
| Compress audio and speech using Least Squares method | *Supervised by Prof. A. Masnadi* |
| Face recolonization on the ORL face database | *Supervised by Msc. M. Faghihi* |
| ADSL protocols (coding, decoding,data transfer, ...) | *Supervised by Prof. H. Keshavarz* |
| Design and Implement of digital lock | *supervised by Prof. M. Farhang* |
| Design and Implement of digital AM modulator | *Supervised by Prof. N. Yasrebi* |
| Design and Implement of efficient vector processor | *Supervised by Prof. N. Yasrebi* |

## TEACHING EXPERIENCE

Field-Programmable Gate Array (FPGA) course instructor Prof. M. Raji

*September 2018 - January 2019*

Digital Logic Circuits course instructor Prof. M. Yazdi

*February 2017 - June 2018*

Digital Logic Circuits Laboratory course instructor Prof. N. Yasrebi

*September 2017 - June 2018*

Probability and Statistics course instructor Prof. M. Derakhtian and Mr. Enjavi

*February 2018 - June 2018*

Digital Communication course instructor Prof. A. Sheikhi and Mr. Enjavi

*February 2018 - June 2018*

Computer Architecture course instructor Prof. N. Yasrebi

*September 2017 - January 2018*

**Programming instructor of software**:

- Matlab
- Xilinx ISE (Verilog)
- Codevision (C)
- Proteus

## WORK EXPERIENCE

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|---|---|
| Data analyst at Decopart group, Shiraz | *December 2020 - present* |
| IT Specialist at IranOnTour team, Shiraz, Iran | *December 2019 - December 2020* |
| Surgical room engineer Assistant at Namazi Hospital, Shiraz, Iran | *June 2018 - December 2018* |

## EXTRACURRICULAR ACTIVITIES

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|---|---|
| Complete Machine Learning course (Certified) | *May 2020* |
| Complete Python beginner to advance course (Certified) | *Jan 2020* |
| Physical Fitness Coaching Certificate | *Nov 2018* |
| Aqua Sport Coaching Certificate | *Apr 2019* |
| Member of the choir music group of Shiraz University | *2014 - 2015* |
| Member of the football team of Shiraz University | *2014 - 2016* |
| Leader of the procurement group of BTIU conference | *2016 (10th Round)* |
| Referee of KAPA Competition | *2017 (7th Round)* |

## LANGUAGES

**Persian**: Native        **English**: Fluent - IELTS 7

## REFERENCES

| | |
|---|---|
| Prof. Navid Yasrebi | nyasrebi@shirazu.ac.ir |
| (assistant professor at electronic department of shiraz university) | +989173094516 |
| Prof. Mohsen Raji | mraji@shirazu.ac.ir |
| (assistant professor at computer department of shiraz university) | +989122263341 |
| Prof. Mehran Yazdi | yazdi@shirazu.ac.ir |
| (associate professor at telecommunication department of shiraz university) | +989173043951 |